



X-1317 US
10/600,846

PATENT
Conf. No. 8489

AF
JPW

IN THE UNITED STATES PATENT OFFICE

Applicant: Raymond Kong
Assignee: Xilinx, Inc.
Title: Integrated Circuit Signal Routing Using Resource Cost Assignment and Costing Data
Serial No.: 10/600,846 File Date: 06-19-2003
Examiner: Vuthe Siek Art Unit: 2825
Docket No.: X-1317 US Conf. No.: 8489

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COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450

RESPONSE TO FINAL OFFICE ACTION

Dear Sir:

In response to the Final Office Action mailed from the Patent Office on July 6, 2005, please consider the following remarks.

The Listing of the Claims No amendments are made to the claims. However, the claims are listed for ease of reference beginning on page 2 of this paper.

Remarks begin on page 8 of this paper.

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V.S.
9/19/05